



**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Batch/Confirmation No.: 6657

Takaaki NODA et al.

Allowed: April 13, 2009

Group Art Unit: 1792

Application No.: 10/587,500

Examiner: K. GAMBETTA

Filed: July 27, 2006

Docket No.: 128807

For: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE AND  
APPARATUS FOR PROCESSING SUBSTRATE

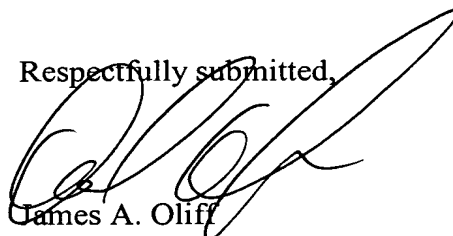
**LETTER TO THE OFFICIAL DRAFTSPERSON**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Please substitute the attached 1 sheet of formal drawings depicting Figure 5 for the  
corresponding drawings filed with the application.

Respectfully submitted,



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JAO:MJS/mcp

Date: June 4, 2009

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